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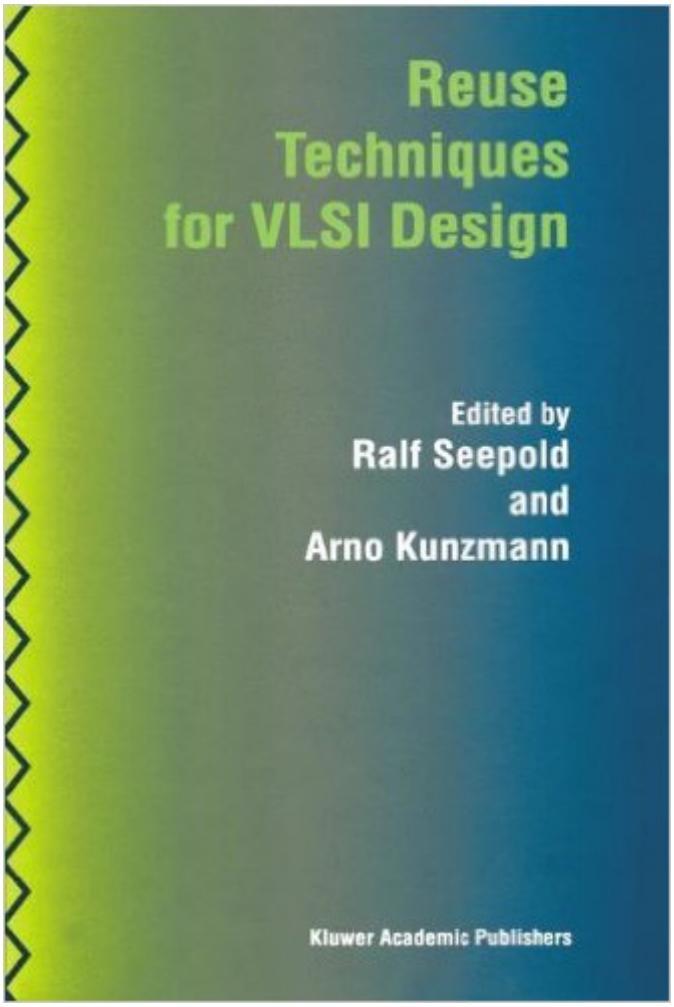
Reuse Techniques For VLSI Design



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Synopsis

Reuse Techniques for VLSI Design is a reflection on the current state of the art in design reuse for microelectronic systems. To that end, it is the first book to garner the input of leading experts from both research and application areas. These experts document herein not only their more mature approaches, but also their latest research results. Firstly, it sets out the background and support from international organisations that enforce System-on-a-Chip (SoC) design by reuse- oriented methodologies. This overview is followed by a number of technical presentations covering different requirements of the reuse domain. These are presented from different points of view, i.e., IP provider, IP user, designer, isolated reuse, intra-company or inter-company reuse. More general systems or case studies, e.g., metrics, are followed by comprehensive reuse systems, e.g., reuse management systems partly including business models. Since design reuse must not be restricted to digital components, mixed- signal and analog reuse approaches are also presented. In parallel to the digital domain, this area covers research in reuse database design. Design verification and legal aspects are two important topics that are closely related to the realization of design reuse. These hot topics are covered by presentations that finalize the survey of outstanding research, development and application of design reuse for SoC design. Reuse Techniques for VLSI Design is an invaluable reference for researchers and engineers involved in VLSI/ASIC design.

Book Information

Hardcover: 153 pages
Publisher: Springer; 1999 edition (March 31, 1999)
Language: English
ISBN-10: 0792384768
ISBN-13: 978-0792384762
Product Dimensions: 6.1 x 0.4 x 9.2 inches
Shipping Weight: 15.2 ounces (View shipping rates and policies)
Average Customer Review: Be the first to review this item
Best Sellers Rank: #5,788,887 in Books (See Top 100 in Books) #46 inÂ Books > Computers & Technology > Programming > Software Design, Testing & Engineering > Software Reuse #233 inÂ Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI #902 inÂ Books > Computers & Technology > Programming > Software Design, Testing & Engineering > Logic

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